L Number	Hits	Search Text	DB	Time stamp
-	10	(parallel\$3 same (cpu processor processing) and (dsp (digital adj2 signal	USPAT;	2003/10/14 14:45
		adj2 processor)) and (load\$3 near3 manag\$5) and communicat\$3 and	US-PGPUB;	
		transmi\$5 and (cpu processor) and ((dma (direct adj2 memory adj2	ЕРО; ЈРО;	
		access)) same register\$3) and fifo and (state adj2 machine) and	DERWENT;	
		(memor\$3 same interfac\$3 same (dma (direct adj2 memory adj2 access))	IBM_TDB	
		same external) and (control\$4 near4 register) and (status near4 register)		
	12	and (dma (direct adj2 memory adj2 access))) (((parallel\$3 same (cpu processor processing)) and (dsp (digital adj2	USPAT;	2003/10/14 14:59
-	12	signal adj2 processor)) and (load\$3 near3 manag\$5) and communicat\$3	US-PGPUB;	2003/10/14 14:57
		and transmi\$5 and (cpu processor) and ((dma (direct adj2 memory adj2	ЕРО; ЛРО;	
		access)) and register\$3) and fifo and (state adj2 machine) and (memor\$3	DERWENT;	
		same interfac\$3 same (dma (direct adj2 memory adj2 access)) and	IBM_TDB	
		external) and (control\$4 near4 register) and (status near4 register) and		
	_	(dma (direct adj2 memory adj2 access)))) not "110"	Y TOD A TO	2002/10/14 14 50
-	2	(((parallel\$3 same (cpu processor processing)) and (dsp (digital adj2	USPAT;	2003/10/14 14:59
		signal adj2 processor)) and (load\$3 near3 manag\$5) and communicat\$3	US-PGPUB; EPO; JPO;	
		and transmi\$5 and (cpu processor) and ((dma (direct adj2 memory adj2 access)) and register\$3) and fifo and (state adj2 machine) and (memor\$3	DERWENT;	,
		same interfac\$3 same (dma (direct adj2 memory adj2 access)) and	IBM TDB	
		external) and (control\$4 near4 register) and (status near4 register) and		
		(dma (direct adj2 memory adj2 access)))) not (((parallel\$3 same (cpu		
		processor processing)) and (dsp (digital adj2 signal adj2 processor)) and		
		(load\$3 near3 manag\$5) and communicat\$3 and transmi\$5 and (cpu		
		processor) and ((dma (direct adj2 memory adj2 access)) same	:	
İ		register\$3) and fifo and (state adj2 machine) and (memor\$3 same interfac\$3 same (dma (direct adj2 memory adj2 access)) same external)		
		and (control\$4 near4 register) and (status near4 register) and (dma		
		(direct adj2 memory adj2 access))))		
_	52	(((dma (direct adj2 memory adj2 access)) same register\$3) and (fifo	USPAT;	2003/10/15 08:01
		same (state adj2 machine)) and (memor\$3 same interfac\$3 same (dma	US-PGPUB;	
		(direct adj2 memory adj2 access)) and external) and (((cpu processor)	EPO; JPO;	
		same ((control\$4 near4 register) and (status near4 register) and (dma	DERWENT;	
		(direct adj2 memory adj2 access))) and (dsp (digital adj2 signal adj2	IBM_TDB	
	48	processor))))) ((real-time (real adj2 time)) same (dsp (digital adj2 signal adj2	USPAT;	2003/10/16 10:19
	1 40	processor)) same load\$3 same manag\$5)	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	2002/10/15 10 15
-	8	(((real-time (real adj2 time)) same (dsp (digital adj2 signal adj2	USPAT;	2003/10/15 10:41
		processor)) same load\$3 same manag\$5)) and ((ip and (sonet (synchronous with optical with network))) (connect\$3 same (fiber-optic	US-PGPUB; EPO; JPO;	
		(synchronous with optical with network))) (connects 3 same (noer-optic (fiber with optic)) same transmi\$5))	DERWENT;	
		(moet with opine)) same nansmin(2))	IBM TDB	
_	1	((((dma (direct adj2 memory adj2 access)) same register\$3) and (fifo	USPAT;	2003/10/15 10:42
		same (state adj2 machine)) and (memor\$3 same interfac\$3 same (dma	US-PGPUB,	
		(direct adj2 memory adj2 access)) and external) and (((cpu processor)	ЕРО; ЛРО;	
		same ((control\$4 near4 register) and (status near4 register) and (dma	DERWENT;	
		(direct adj2 memory adj2 access))) and (dsp (digital adj2 signal adj2	IBM_TDB	
		processor)))))) and ((ip and (sonet (synchronous with optical with network))) (connect\$3 same (fiber-optic (fiber with optic)) same		
		transmi\$5))		
_	271	(dsp (digital adj2 signal adj2 processor)) and ((ip and (sonet	USPAT,	2003/10/15 10:43
	2.1	(synchronous with optical with network))))	US-PGPUB;	
		-	ЕРО; ЈРО;	
			DERWENT;	
			IBM_TDB	2002/10/15 10:50
-	37	(dsp (digital adj2 signal adj2 processor)) and ((ip and (sonet	USPAT; US-PGPUB;	2003/10/15 10:59
		(synchronous with optical with network))) and (connect\$3 same (fiber-optic (fiber with optic)) same transmi\$5))	EPO IPO	
		(noci-optic (noci with optic)) same transmiss))	DORWENT;	
		BEST AVAILABLE	IBM_TDB	
L	1		·	···

-	1	((dsp (digital adj2 signal adj2 processor)) and ((ip and (sonet	USPAT;	2003/10/15 11:00
		(synchronous with optical with network))) (connect\$3 same (fiber-optic	US-PGPUB;	
		(fiber with optic)) same transmi\$5))) and foip	ЕРО; ЛРО;	
			DERWENT;	
			IBM TDB	
-	1 1	(((dsp (digital adj2 signal adj2 processor)) and ((ip and (sonet	USPĀT;	2003/10/15 11:01
	_	(synchronous with optical with network))) (connect\$3 same (fiber-optic	US-PGPUB;	
		(fiber with optic)) same transmi\$5))) and foip) and (transmi\$5	ЕРО; ЛРО;	
	[communicat\$3 data medium)	DERWENT;	
		communicates data medium)	IBM TDB	
	52	(((due (direct ali) mamoru adi) access)) some register(2) and (fife	USPAT;	2003/10/16 10:18
-	32	(((dma (direct adj2 memory adj2 access)) same register\$3) and (fifo		2003/10/10 10.16
		same (state adj2 machine)) and (memor\$3 same interfac\$3 same (dma	US-PGPUB;	
		(direct adj2 memory adj2 access)) and external) and (((cpu processor)	ЕРО; ЛРО;	
		same ((control\$4 near4 register) and (status near4 register) and (dma	DERWENT;	
		(direct adj2 memory adj2 access))) and (dsp (digital adj2 signal adj2	IBM_TDB	
		processor)))))		
-	0	((((dma (direct adj2 memory adj2 access)) same register\$3) and (fifo	USPAT;	2003/10/16 10:51
		same (state adj2 machine)) and (memor\$3 same interfac\$3 same (dma	US-PGPUB;	
		(direct adj2 memory adj2 access)) and external) and (((cpu processor)	ЕРО; ЈРО;	
		same ((control\$4 near4 register) and (status near4 register) and (dma	DERWENT;	
		(direct adj2 memory adj2 access))) and (dsp (digital adj2 signal adj2	IBM_TDB	
		processor)))))) and ((ping-pong (ping adj2 pong)) with memory)		
-	52	((real-time (real adj2 time)) same (dsp (digital adj2 signal adj2	USPAT;	2003/10/16 10:19
		processor)) same load\$3 same manag\$5)	US-PGPUB;	
Ì			ЕРО; ЈРО;	
			DERWENT;	
			IBM_TDB	
1-	3	(((real-time (real adj2 time)) same (dsp (digital adj2 signal adj2	USPAT;	2003/10/16 10:20
	}	processor)) same load\$3 same manag\$5)) and ((ping-pong (ping adj2	US-PGPUB;	
		pong)) with memory)	ЕРО, ЈРО,	
			DERWENT;	
			IBM TDB	
_	274	((dsp (digital adj2 signal adj2 processor)) same load\$3 same manag\$5)	USPĀT;	2003/10/16 10:20
			US-PGPUB,	
			ЕРО; ЈРО;	
			DERWENT;	
			IBM_TDB	
_	7	(((dsp (digital adj2 signal adj2 processor)) same load\$3 same manag\$5)	USPAT;	2004/07/08 10:17
) and ((ping-pong (ping adj2 pong)) with memory)	US-PGPUB;	
			ЕРО, ЈРО,	
		,	DERWENT,	
			IBM TDB	
-	4	((((dsp (digital adj2 signal adj2 processor)) same load\$3 same	USPAT;	2003/10/16 10:20
		manag\$5)) and ((ping-pong (ping adj2 pong)) with memory)) not	US-PGPUB;	
		(((((real-time (real adj2 time)) same (dsp (digital adj2 signal adj2	ЕРО; ЈРО;	
		processor)) same load\$3 same manag\$5)) and ((ping-pong (ping adj2	DERWENT;	
		pong)) with memory))	IBM_TDB	
_	134	(dsp (digital adj2 signal adj2 processor)) and ((ping-pong (ping adj2	USPAT;	2003/10/16 10:53
		pong)) with memory)	US-PGPUB;	
1		· -··	ЕРО, ЈРО;	
			DERWENT,	
			IBM_TDB	
-	0	((((dma (direct adj2 memory adj2 access)) same register\$3) and (fifo	USPAT;	2003/10/16 11:06
		same (state adj2 machine)) and (memor\$3 same interfac\$3 same (dma	US-PGPUB;	
		(direct adj2 memory adj2 access)) and external) and (((cpu processor)	ЕРО; ЛРО;	
		same ((control\$4 near4 register) and (status near4 register) and (dma	DERWENT;	
		(direct adj2 memory adj2 access))) and (dsp (digital adj2 signal adj2	IBM_TDB	
1		processor))))) and ((ping-pong (ping adj2 pong)) with buffer\$3)		
-	1	((((dma (direct adj2 memory adj2 access)) same register\$3) and (fifo	USPAT;	2003/10/16 10:52
		same (state adj2 machine)) and (memor\$3 same interfac\$3 same (dma	US-PGPUB;	
1		(direct adj2 memory adj2 access)) and external) and (((cpu processor)	ЕРО; ЈРО;	
		same ((control\$4 near4 register) and (status near4 register) and (dma	DERWENT;	
		(direct adj2 memory adj2 access))) and (dsp (digital adj2 signal adj2	IBM_TDB	
		processor)))))) and (thread\$3 with memory)	ALL ARL	E COPY
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	44	(((1 (1) (1 1) 1 1) 1 1) 1 1) 1 10 (65)	TIODAT	2003/10/16 13:20
-	44	(((dsp (digital adj2 signal adj2 processor)) same load\$3 same manag\$5)	USPAT;	2003/10/16 13:20
) and (thread\$3 with memory)	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
	7	(1(1(-11111	IBM_TDB	2003/10/16 11:16
-	7	(dsp (digital adj2 signal adj2 processor)) and (MUX near5 (external	USPAT;	2003/10/10 11.10
		near5 memory))	US-PGPUB;	
			ЕРО; ЛРО;	
			DERWENT;	
	241	(1 (1 (1 12 1 1 12)) 1/1/17/17 5	IBM_TDB	2003/10/16 11:19
-	241	(dsp (digital adj2 signal adj2 processor)) and (MUX near5 memory)	USPAT;	2003/10/10 11.19
			US-PGPUB;	
	i		ЕРО; ЛРО;	
			DERWENT;	
		(1 (V) (1 V) 1 V) 1 V) 1 V(1 (T) V)	IBM_TDB	2002/10/17 11 20
-	138	(dsp (digital adj2 signal adj2 processor)) and (MUX same memory	USPAT;	2003/10/16 11:20
		same external)	US-PGPUB;	
			ЕРО; ЛРО;	
			DERWENT;	
			IBM_TDB	
-	27	((((dsp (digital adj2 signal adj2 processor)) same load\$3 same	USPAT;	2004/07/08 09:54
	}	manag\$5)) and (thread\$3 with memory)) and (mac ((memory with	US-PGPUB;	
		access with control\$4) same array) or (memory with (control\$4	ЕРО; ЛРО;	
		manag\$5) same external))	DERWENT,	
			IBM_TDB	
-	26	((((dsp (digital adj2 signal adj2 processor)) same load\$3 same	USPAT;	2003/10/16 14:25
		manag\$5)) and (thread\$3 with memory)) and ((mac (memory with	US-PGPUB;	
		access with control\$4)) and external)	ЕРО; ЈРО;	
1			DERWENT;	
			IBM_TDB	
_	33	(((real-time (real adj2 time)) same (dsp (digital adj2 signal adj2	USPAT;	2003/10/16 14:25
		processor)) same load\$3 same manag\$5)) and ((mac (memory with	US-PGPUB;	
		access with control\$4)) and external)	ЕРО; ЈРО;	
			DERWENT;	
			IBM_TDB	
-	33	((dsp (digital adj2 signal adj2 processor)) same load\$3 same (balanc\$3	USPAT;	2004/07/08 13:23
1		manag\$5)) and (parallel\$3 with (schedul\$3 process\$3)) and ((dma	US-PGPUB;	
		(direct adj2 memory adj2 access\$3)) same (queu\$3 fifo first-in-first-out	EPO; JPO;	
		(first adj in adj first adj out)) same (register\$3 (high near3 speed\$3	DERWENT;	
		near3 (storage memor\$3))))	IBM_TDB	
-	14	(("6104721" "4991169" "5724587" "5440740" "5842014" "5291614"	USPAT;	2004/07/21 08:40
		"5822606") .pn.) and ((parallel\$5 with process\$3) (DMa (direct near3	US-PGPUB;	
		memory near3 access\$3)) register\$3 (queu\$3 fifo first-in-first-out (first	ЕРО; ЈРО;	
		adj in adj first adj out)) (cpu dsp (central adj process\$3 adj unit) (digital	DERWENT;	
		adj signal adj process\$3)) ((stat\$3 with machine)) and interfac\$3)	IBM_TDB	
_	6	(("6104721" "4991169" "5724587" "5440740" "5842014" "5291614"	USPAT;	2004/07/20 14:55
		"5822606") .pn.) and (external)	US-PGPUB;	
			ЕРО; ЛРО;	
			DERWENT;	
		•	IBM_TDB	
_	173	((DMa (direct near3 memory near3 access\$3)) same register\$3 same	USPAT;	2004/07/20 16:55
		(queu\$3 fifo first-in-first-out (first adj in adj first adj out)) same (stat\$3	US-PGPUB;	
		with machine))	ЕРО; ЛРО;	•
		<i>"</i>	DERWENT;	
[IBM_TDB	

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